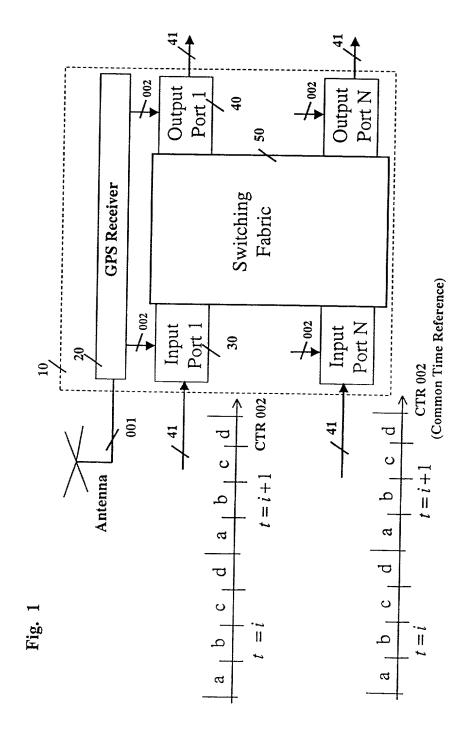
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APPLICANT/INVENTOR: YORAM OFEK
LAW FIRM: SITRICK & SITRICK
ATTORNEY DOCKET NO.: SYN 1790
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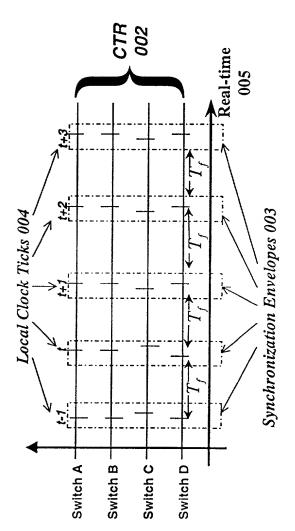


FIG. 2

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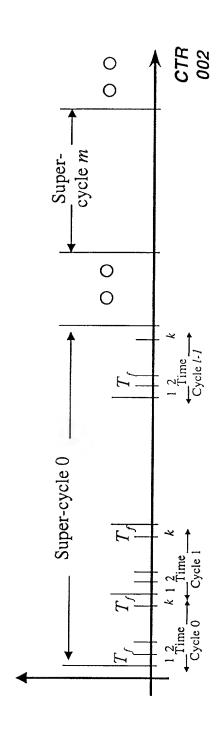


FIG. 3

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CTR is UTC 0 005 Time-frames beginning of a second Super-cycle m with 8k beginning of a second 0 001 Time Cycle 79 with 8k Time-frames Super-cycle 0 100 Time 1001 2 of a second beginning Time_ Cycle0_

FIG.

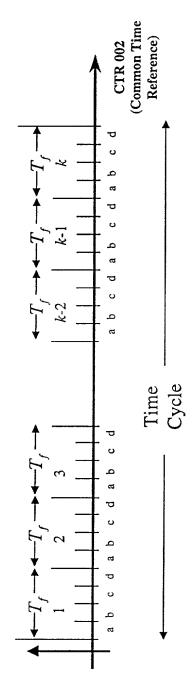


FIG. 5

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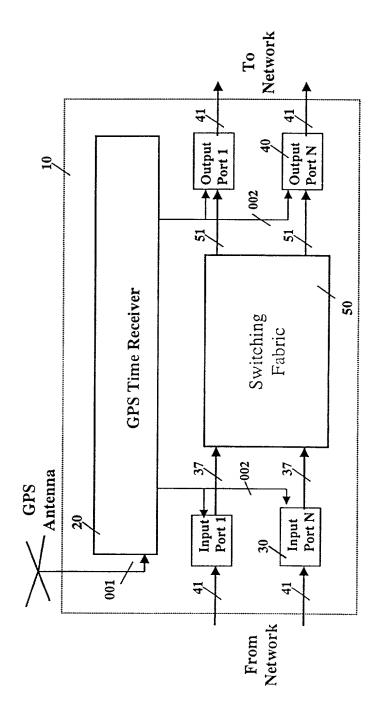
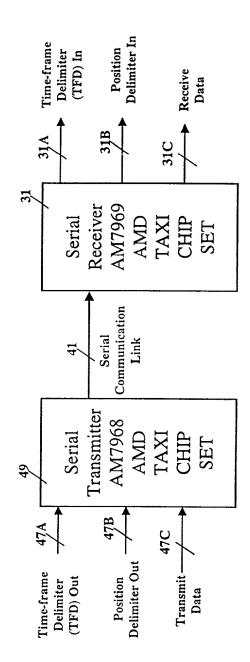


FIG. 6

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FIG

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4B,	4B/5B encoding scheme	g scheme
HEX	4-bit	5-bit Encoded
DATA	Binary Data	Data Codeword
0	0000	11110
	0001	01001
	0010	10100
	0011	10101
	0100	01010
	0101	01011
	0110	01110
	0111	01111
%	1000	10010
	1001	10011
	1010	10110
e	1011	10111
· ·	1100	11010
Ω	1101	11011
£.)	1110	11100
F	1111	11101

DIVISIONAL PATENT APPLICATION APPLICANT/INVENTOR: YORAM OFEK LAW FIRM: SITRICK & SITRICK ATTORNEY DOCKET NO.: SYN 1790 SHEET 9 OF 24

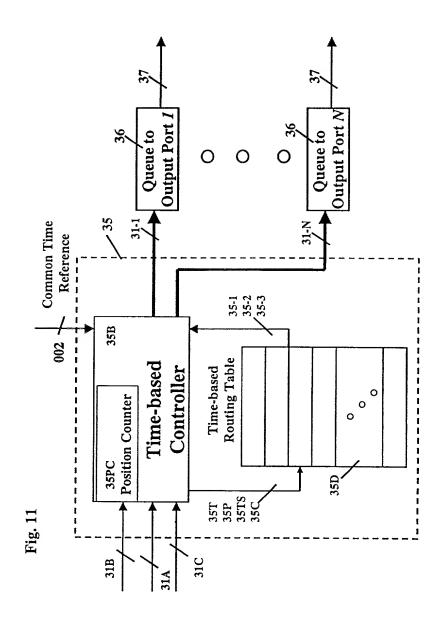
4B/5B encoding scheme	10-bit	Encoded Control	Codeword		11111 11111	01101 01101	01101 11001	11111 00100	01101 00111	11001 00111					00111 00111	00111 11001	00000 00100	00000 11111	00000 00000
	Control Input		Binary	Data	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
			HEX	DATA	1	2	6	4	v	9	7	∞	6	¥	æ	ပ	Ω	田	<u></u>

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LAW FIRM: SITRICK & SITRICK
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37 007 31-N Queen Output Port M 31-1 Queué to Output Port Common Time Reference 00 Fime-based Routing . Controller 30 31A 31B 31C Receiver Serial

FIG. 10

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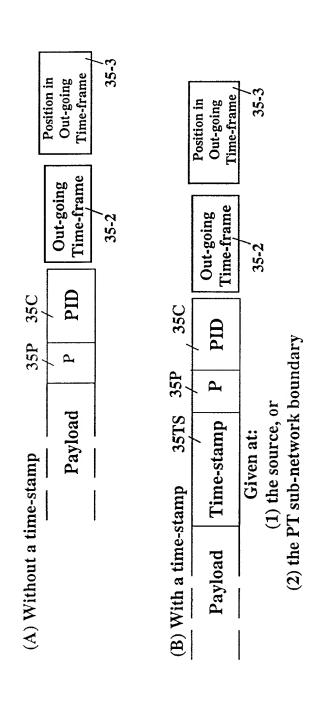
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POSITION IN OUT-GOING TIME-FRAME 35-3 α p ರ OUT-GOING TIME-FRAME $t+3 \mod k$ $t+5 \mod k$ t+4 mod k $t+3 \mod k$ 35-2 OUTPUT PORT 35-1 S 4 3 35D Time-frame of Arrival Position Counter 35P (TOA) 35T, 35C

Fig. 1

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FIG. 13



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FIG. 14

• Real-time protocol (RTP) with the following fields in the header:

• version (V) - 2 bits

• padding (P) - 1 bit

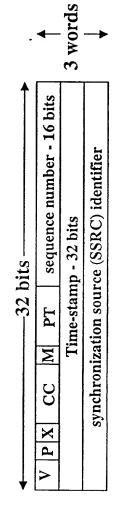
• extension (X) - 1 bit

• CSRC count (CC) - 4 bits

• marker (M) - 1 bit

• payload type - 7 bits • sequence number - 16 bits

• times-tamp - 32 bits

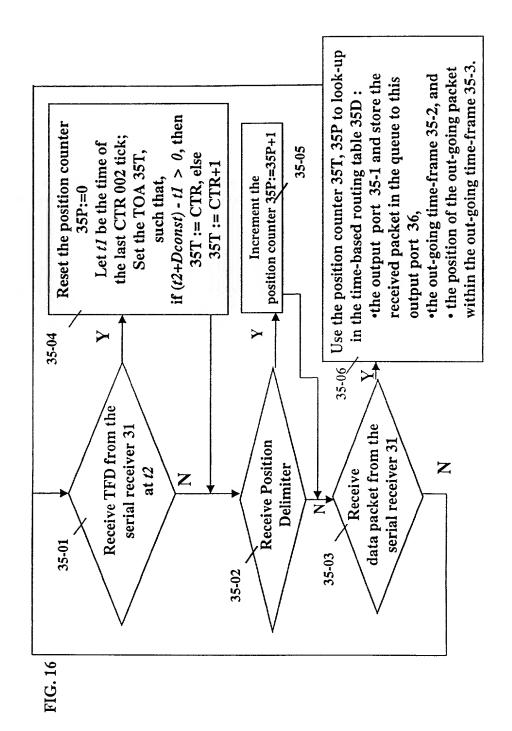


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POSITION IN OUT-GOING TIME-FRAME 35-3 ಡ p P ರ OUT-GOING TIME-FRAME $t+3 \mod k$ t+5 mod k $t+3 \mod k$ t+4 mod k 35-2 OUTPUT PORT 35-1 S 3 4 Virtual Pipe ID (PID) 35C 35D Position Counter 35P, Time Stamp 35TS, 35C

FIG. 1:

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DIVISIONAL PATENT APPLICATION
APPLICANT/INVENTOR: YORAM OFEK
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Transmitter Serial 6 47A 47B 47C Scheduling Controller and Transmit Buffer 40 002 Common Time Reference Data Packets From the Switching CTR

FIG. 1'

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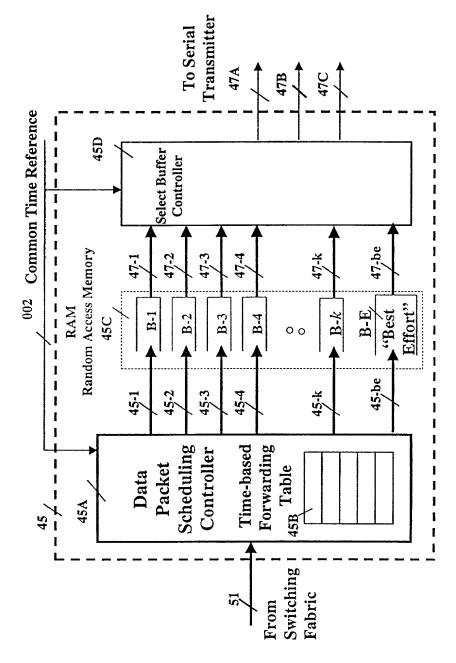


FIG. 18

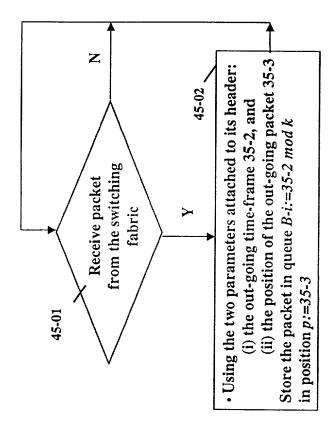
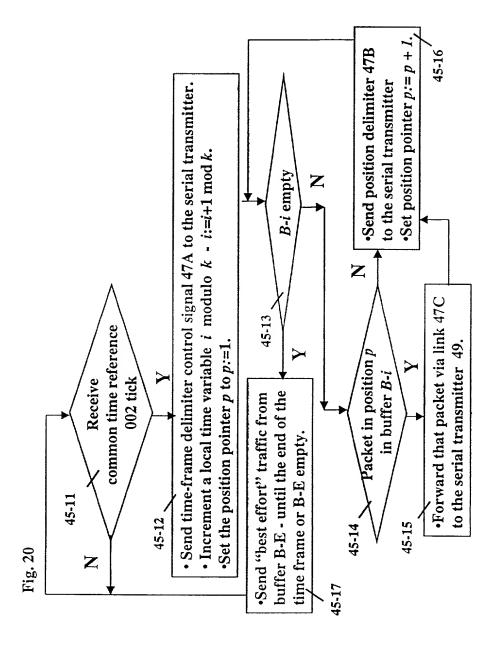


Fig.



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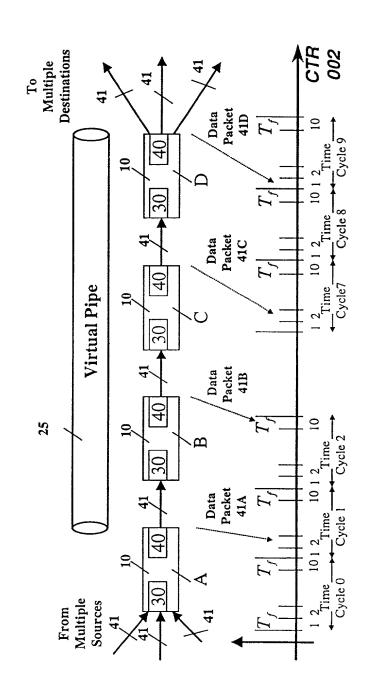


FIG. 21

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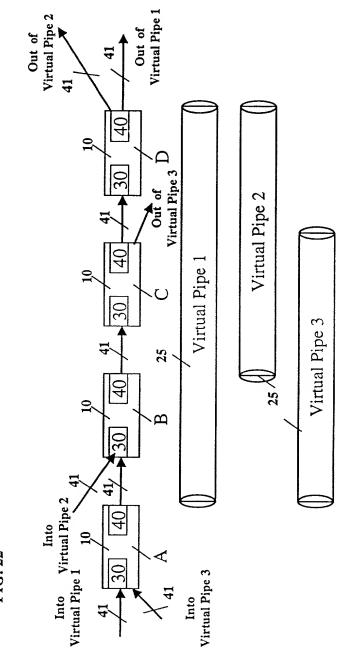


FIG. 27

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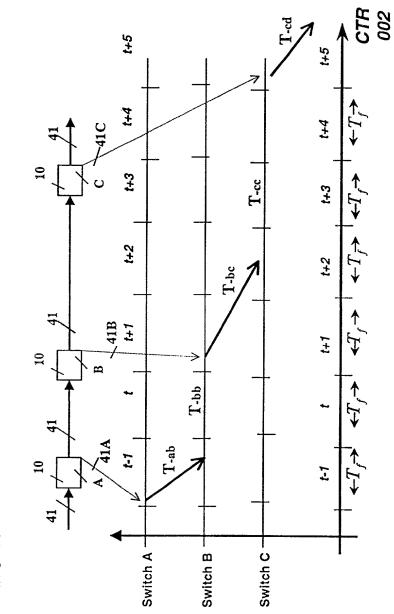


FIG. 23

FIG. 24

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